



Serial No. 09/648164

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

#13/24
V. B. Ryan
3/4/03

Patent Application

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Hergenrother, John
Monroe, Donald

Case: **15-6-9**

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Group Art Unit: **2826**

Title: **Architecture for Circuit Connection Of A Vertical Transistor**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks Washington, D.C. 20231 on

2-13-03

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Mary V. Carter
Signature of person mailing paper or fee

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D. C. 20231-

SIR:

PETITION FOR EXTENSION OF TIME

Applicant(s) petition(s) the Assistant Commissioner For Patents to extend the time for response to the Office action dated 10/22/02, for 1 month(s) from 1/22/03 to 02/22/03. Please charge Agere Deposit Account No. 501735 in the amount of \$110.00 to cover the cost of the extension. Any deficiency or overpayment should be charged or credited to Deposit Account No. 501735. Duplicate copies of this petition are enclosed.

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01 FC:1251 110.00 CH

Respectfully,

Ferdinand M. Romano
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Dated: 13 February 2003
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